CLAIMS

What is claimed is:

1. A computer implemented method comprising:

generating a first command for a set of network data to be executed on a local memory;

executing a second command for the set of network data on a remote memory in response to generation of the first command;

determining whether the second command has been executed successfully on the remote memory;

executing the first command on the local memory upon determining the second command is executed successfully; and

generating an error upon determining the second command is not executed successfully.

2. The computer implemented method of claim 1 wherein the set of network data is a set of configurations.

3. The computer implemented method of claim 1 wherein the first command is a write command to an address of the local memory and the second command is a write command to an address of the remote memory.

4. The computer implemented method of claim 1 wherein the first command is a delete command for an address of the local memory and the second command is a delete command for an address of the remote memory.

5. A computer implemented method comprising:

mapping a first memory to a second memory;

receiving a set of configurations;

processing the set of configurations;

generating a set of commands for the first memory, the set of commands corresponding to the processed set of configurations;

triggering an exception when beginning to execute the set of commands on the first memory, wherein the exception performs the following:

executing the set of commands on the second memory in response to the exception;

upon determining the set of commands are executed successfully on the second memory, completing execution of the set of commands on the first memory; and

upon determining the set of commands are not executed on the second memory successfully, generating an error.

6. The computer implemented method of claim 5 wherein the set of configurations are for a set of network processes.

7. The computer implemented method of claim 5 wherein the error is displayed as a text message.

8. The computer implemented method of claim 5 wherein the error is passed to an error parser.

9. The computer implemented method of claim 5 wherein the mapping the first memory to the second memory comprises associating a set of addresses of the first memory to a set of addresses of the second memory.

10. The computer implemented method of claim 5 wherein the set of commands are a set of write commands.

11. The computer implemented method of claim 5 wherein the set of commands are a set of delete commands.

12. A computer implemented method comprising:

receiving a request to modify configuration data located at a local address in local memory in an active control card;

generating an exception when the configuration data located at the local address in the local memory is attempted to be modified, wherein the exception performs, within a processor in the active control card, the following:

modifying configuration data located at a remote address in remote memory in an inactive control card, wherein the configuration data located at the local address corresponds to the configuration data located at the remote address;

modifying the configuration data located at the local address in local memory in the active control card upon determining that the modification of the configuration data located at the remote address in the remote memory in the inactive control card was successful; and

generating an error without modifying the configuration data located at the local address in local memory in the active control card upon determining that the modification of the configuration data located at the remote address in the remote memory in the inactive control card was not successful.

13. The computer implemented method of claim 12 wherein the request is to write the configuration data.

14. The computer implemented method of claim 12 wherein the request is to delete the configuration data.

15. The computer implemented method of claim 12 wherein the error is displayed on an interface.

16. The computer implemented method of claim 12 wherein the error is passed to an error parser.

17. An apparatus comprising:

an interface to receive a set of configurations;

a first control card coupled to the interface, the first control card having a first memory;

a second control card coupled to the interface and the first control card, the second control card to receive the set of configurations from the interface, to generate a set of commands for the set of configurations, to execute the set of commands on the first memory in response to an exception, and to execute the set of commands on a second memory of the second control card.

18. The apparatus of claim 17 wherein the first memory and the second memory are a first main memory and a second main memory.

19. The apparatus of claim 17 wherein the first memory and the second memory are a first storage and a second storage.

20. The apparatus of claim 17 wherein the first memory is mapped into the second memory.

21. The apparatus of claim 17 further comprising the second control card to generate an error if the set of commands are not executed successfully on the first memory.

22. The apparatus of claim 17 wherein the set of commands are a set of write commands.

23. The apparatus of claim 17 wherein the set of commands are a set of delete commands.

24. An apparatus comprising:

a processor to execute a configuration process, the configuration process to receive a set of configurations, process the set of configurations, and to submit a set of requests to the processor;

a first memory coupled to the processor, the processor to perform the set of requests on the first memory in response to an exception triggered by the set of requests; and

a second memory coupled to the first memory and the processor, the processor to perform the set of requests on the second memory if the set of requests are performed successfully on the first memory.

25. The apparatus of claim 24 wherein the first memory and the second memory are a first main memory and a second main memory.

26. The apparatus of claim 24 wherein the first memory and the second memory are a first storage and a second storage.

27. The apparatus of claim 24 wherein the set of requests are a set of write commands.

28. The apparatus of claim 24 wherein the set of requests are a set of delete commands.

29. The apparatus of claim 24 further comprising the processor to generate an error if the set of requests are not performed on the first memory successfully.

30. The apparatus of claim 24 further comprising a second processor coupled to the first processor, the first memory, and the second memory.

31. A machine-readable medium that provides instructions, which when executed by a set of processors of one or more processors, cause said set of processors to perform operations comprising:

generating a first command for a set of network data to be executed on a local memory;

executing a second command for the set of network data on a remote memory in response to generation of the first command;

determining whether the second command has been executed successfully on the remote memory;

executing the first command on the local memory upon determining the second command is executed successfully; and

generating an error upon determining the second command is not executed successfully.

32. The machine-readable medium of claim 31 wherein the set of network data is a set of configurations.

33. The machine-readable medium of claim 31 wherein the first command is a write command to an address of the local memory and the second command is a write command to an address of the remote memory.

34. The machine-readable medium of claim 31 wherein the first command is a delete command for an address of the local memory and the second command is a delete command for an address of the remote memory.

35. A machine-readable medium that provides instructions, which when executed by a set of processors of one or more processors, cause said set of processors to perform operations comprising:

associating a first memory to a second memory;

receiving a set of configurations;

processing the set of configurations;

generating a set of commands for the first memory, the set of commands corresponding to the processed set of configurations;

triggering an exception when processing the set of commands for the first memory, wherein the exception performs the following:

executing the set of commands on the second memory;

upon determining the set of commands are executed successfully on the second memory, executing the set of commands on the first memory; and

upon determining the set of commands are not executed on the second memory successfully, generating an error.

36. The machine-readable medium of claim 35 wherein the set of configurations are for a set of network processes.

37. The machine-readable medium of claim 35 wherein the error is displayed as a text message.

38. The machine-readable medium of claim 35 wherein the error is passed to an error parser.

39. The machine-readable medium of claim 35 wherein the associating the first memory to the second memory comprises associating a set of addresses of the first memory to a set of addresses of the second memory.

40. The machine-readable medium of claim 35 wherein the set of commands are a set of write commands.

41. The machine-readable medium of claim 35 wherein the set of commands are a set of delete commands.

42. A machine-readable medium that provides instructions, which when executed by a set of processors of one or more processors, cause said set of processors to perform operations comprising:

receiving a request to modify configuration data located at a local address in local memory in an active control card;

generating an exception when the configuration data located at the local address in the local memory is attempted to be modified, wherein the

exception performs, within a processor in the active control card, the following:

modifying configuration data located at a remote address in remote memory in an inactive control card, wherein the configuration data located at the local address corresponds to the configuration data located at the remote address;

modifying the configuration data located at the local address in local memory in the active control card upon determining that the modification of the configuration data located at the remote address in the remote memory in the inactive control card was successful; and

generating an error without modifying the configuration data located at the local address in local memory in the active control card upon determining that the modification of the configuration data located at the remote address in the remote memory in the inactive control card was not successful.

43. The machine-readable medium of claim 42 wherein the request is to write the configuration data.

44. The machine-readable medium of claim 42 wherein the request is to delete the configuration data.

45. The machine-readable medium of claim 42 wherein the error is displayed on an interface.

46. The machine-readable medium of claim 42 wherein the error is passed to an error parser.